# LISTA DE LUCRĂRI

## conf.dr.ing. Lucian PRODAN

### Teza de doctorat

T1. **Lucian Prodan**. Self-Repairing Memory Arrays Inspired by Biological Processes. Teza de doctorat, Universitatea "Politehnica" din Timisoara, octombrie 2005 (ordinul Ministrului Educatiei si Cercetarii 3824/03.05.2006, 37328 bis/20.09.2006). **[1]**

### Cărţi şi îndrumătoare publicate

C1. **Lucian Prodan**. Self-Repairing Memory Arrays Inspired by Biological Processes. Editura Politehnica, Timisoara, 2009, 211 pagini, ISBN 978-973-625-820-6. **[2.2]**

C2. Mihai Udrescu, **Lucian Prodan.** Emerging Computing Systems: Quantum Computing From a Computer Engineering Perspective. Editura Politehnica, Timisoara, 2013, 128 pagini (12 contributie proprie), ISBN 978- 606-554-684-4. **[2.2]**

C3. Flavius Opritoiu, Mihai Udrescu, **Lucian Prodan**. Arhitectura sistemelor de calcul. Probleme de design si implementare. Editura Politehnica, Timisoara, 2013, 136 pagini (56 contributie proprie), ISBN 978- 606-554-566-3. **[2.2]**

C4. **Lucian Prodan**. Controlul microprogramat in sistemele de calcul. Editura Politehnica, Timisoara, 2023, 103 pagini, ISBN 978-606-35-0511-9. **[2.2]**

I1. Mihai Udrescu, **Lucian Prodan**. Arhitectura calculatoarelor. Indrumator de laborator, 127 pagini (61 contributie proprie). http://www.acsa.upt.ro/teaching/lab_tutorials.htm

### Articole publicate în reviste de specialitate recunoscute

R1. **Lucian Prodan**, Gianluca Tempesti, Daniel Mange, Andre Stauffer. Embryonics: Artificial Cells Made of Artificial Molecules. Transactions on Automatic Control and Computer Science, vol. 45(59), Universitatea Politehnica din Timisoara, Romania, pp. 99-104, 2000, ISSN 1224-600X. **[Jurnal CNCSIS categoria B] [3.6a]**

R2. **Lucian Prodan**, Mihai Udrescu, Oana Boncalo, Mircea Vladutiu. Design for Dependability in Emerging Technologies. ACM Journal of Emerging Technologies in Computing, Volume 3, Issue 2, Article 6, July 2007, ISSN 1550-4832. **[Jurnal Q1 indexat ISI, factor de impact 0.759] [3.1a]**

R3. Mihai Udrescu, Lucian Prodan, Mircea Vladutiu. Simulated fault injection methodology for gate-level quantum circuit reliability assessment. Simulation Modelling Practice and Theory, Elsevier, Amsterdam, The Netherlands, Volume 23, Issue 4, April 2012, pp. 60-70, ISSN 1569-190X. **[Jurnal Q2 indexat ISI, factor de impact 1.173] [3.1a]**

R4. Cristian Cosariu, Alexandru Iovanovici, **Lucian Prodan**, Mircea Vladutiu. TACTICS: Adaptive Framework for Reactive Control of Road Traffic Systems. Scientific Bulletin of the Politehnica University of Timisoara, Transactions on Electronics and Communications, vol. 60(74), 2015, pp. 21-26, ISSN 1583-3380. **[Jurnal CNCSIS categoria B] [3.6a]**

R5. Alexandru Iovanovici, Dacian Avramoni, Lucian Prodan. A dataset of urban traffic flow for 13 Romanian cities amid lockdown and after ease of COVID19 related restrictions. Elsevier Data in Brief, Accepted 2020, ISSN: 2352-3409. **[Jurnal indexat ISI, fara factor de impact] [3.1a]**

## Articole publicate în volumele unor conferinţe internaţionale recunoscute

Vi1. **Lucian Prodan**, Gianluca Tempesti, Daniel Mange, Andre Stauffer. Biology Meets Electronics: The Path to a Bio-Inspired FPGA. Proc. 3rd International Conference on Evolvable Systems (ICES2000), Edinburgh, Scotland, 2000, Springer LNCS 1801, pp. 187-196, ISBN 3-540-67338-5. **[ISI Proceedings] [3.2a]**

Vi2. **Lucian Prodan**, Gianluca Tempesti, Daniel Mange, Andre Stauffer. Embryonics: Artificial Cells Driven by Artificial DNA. Proc. 4th International Conference on Evolvable Systems (ICES2001), Tokyo, Japan, Oct 3-5 2001, Springer LNCS 2210, pp. 100-111, ISBN 3-540-42671-X. **[DBLP, SpringerLink] [3.3b]**

Vi3. **Lucian Prodan**, Gianluca Tempesti, Daniel Mange, Andre Stauffer. Embryonics: Electronic Stem Cells. Proc. AAAI 8th International Conference on Artificial Life, (MIT Press, Cambridge MA, 2003), Sydney, Australia, Dec 9-13, 2002, pp. 101-105, ISBN 0-262-69281-3. **[ACM, CiteSeer, ScientificCommons] [3.3b]**

Vi4. **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. Self-Repairing Embryonic Memory Arrays. Proc. IEEE NASA/DoD Conference on Evolvable Hardware (EH'04), Seattle WA, USA, 2004, pp. 130-137, ISBN 0-7695-2145-2. **[ISI Proceedings] [3.2a]**

Vi5. Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Using HDLs for Describing Quantum Circuits: A Framework for Efficient Quantum Algorithm Simulation. Proc. 1st ACM Conference on Computing Frontiers, Ischia, Italy, April 14 - 16, 2004, pp. 96-110, ISBN 1-58113-741-9. **[INSPEC] [3.3b]**

Vi6. **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. Survivability of Embryonic Memories: Analysis and Design Principles. Proc. IEEE NASA/DoD Conference on Evolvable Hardware (EH'05), Washington DC, USA, 2005, pp. 280-289, ISBN 0-7695-2399-4. **[ISI Proceedings] [3.2a]**

Vi7. **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. Multiple-Level Concatenated Coding in Embryonics: A Dependability Analysis. Proc. GECCO (ACM-SIGEVO), Washigton DC, USA, 2005, pp. 941-948, ISBN 1-59593-010-8. **[ISI Proceedings de top] [3.2a]**

Vi8. Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Simulated Fault Injection in Quantum Circuits with the Bubble Bit Technique. Proc. ICANNGA'05, Coimbra, Portugal, 2005, Springer "Adaptive and Natural Computing Algorithms", pp. 276-279, ISBN 3-211-24934-6. **[ISI Proceedings] [3.2a]**

Vi9. Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. The Bubble Bit Technique as Improvement of HDL-Based Quantum Circuits Simulation. Proc. IEEE 38th Annual Simulation Symposium, San Diego CA, USA, 2005, pp. 217-224, ISBN 0-7695-2322-6. **[ISI Proceedings] [3.2a]**

Vi10. **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. Reliability Assessment in Embryonics Inspired by Fault-Tolerant Quantum Computation. Proc. 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, May 4 - 6 2005, pp.323-333, ACM Press ISBN 1-59593-018-3. **[INSPEC] [3.3b]**

Vi11. Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Improving Quantum Circuit Dependability with Reconfigurable Quantum Gate Arrays. Proc. 2nd ACM International Conference on Computing Frontiers (CF'05), Ischia, Italy, May 4 - 6 2005, pp. 133-144, ACM Press ISBN 1-59593-018-3. **[INSPEC] [3.3b]**

Vi12. Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Implementing Quantum Genetic Algorithms: A Solution Based on Grover's Algorithm. Proc. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 71-82, ACM Press, ISBN 1-59593-302-6. **[INSPEC] [3.3b]**

Vi13. **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. A Dependability Perspective on Emerging Technologies. Proc. 3rd ACM International Conference on Computing Frontiers (CF'06), Ischia, Italy, May 2 - 5 2006, pp. 187-198, ACM Press, ISBN 1-59593-302-6. **[INSPEC] [3.3b]**

Vi14. Oana Boncalo, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu, Alexandru Amaricai. Assessing Quantum Circuits Reliability with Mutant-Based Simulated Fault Injection. Proc. 18th European Conference on Circuit Theory and Design (ECCTD'07), Seville, Spain, August 26-30 2007, pp. 942-945, ISBN 1-4244-1342-7. **[ISI Proceedings] [3.2a]**

Vi15. Oana Boncalo, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu, Alexandru Amaricai. Saboteur Based Fault Injection for Quantum Circuits Fault Tolerance Assessment. Proc.s 10th EUROMICRO Conference

on Digital System Design, Architectures, Methods and Tools (DSD 2007), 2007, Lübeck, Germany, pp. 634-640, ISBN 0-7695-2978-X. **[ISI Proceedings] [3.2a]**

Vi16. Oana Boncalo, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu, Alexandru Amaricai. Using Simulated Fault Injection for Fault Tolerance Assessment of Quantum Circuits. Proc. IEEE 40th Annual Simulation Symposium, Norfolk VA, USA, March 26 - 28, 2007, pp. 213-220, ISSN 1080-241X, ISBN 0-7695-2814-7. **[ISI Proceedings] [3.2a]**

Vi17. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Automatic Synthesis for Quantum Circuits using Genetic Algorithms. Proc. ICANNGA'07 "Adaptive and Natural Computing Algorithms", LNCS 4431 (Springer-Verlag Berlin Heidelberg), Warsaw, Poland, 2007, pp. 174–183, ISBN 978-3-540-71589-4. **[ISI Proceedings] [3.2a]**

Vi18. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. A Genetic Algorithm Framework Applied to Quantum Circuit Synthesis. Proc. Intl. Workshop on Nature Inspired Cooperative Strategies for Optimization (NICSO07), Acireale, Italy, 2007, ISBN 978-3-540-78986-4, published in Studies in Computational Intelligence (SCI) 129, pp. 419-429, ISSN 1860-949X. **[ISI Proceedings] [3.2a]**

Vi19. Oana Boncalo, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu, Alexandru Amaricai. Simulated Fault Injection for Quantum Circuits Based on Simulator Commands. Proc. 4th Intl. Symposium on Applied Computational Intelligence and Informatics (SACI 2007), Timisoara, Romania, 2007, pp. 245-250. **[ISI Proceedings] [3.2b]**

Vi20. **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. Fault-Tolerant Memory Design and Partitioning Issues in Embryonics. Proc. 8th International Conference on Evolvable Systems: From Biology to Hardware (ICES 2008), LNCS (Springer), vol. 5216, Prague, Czech Republic, September 21-24 2008, pp. 372-381, ISSN: 0302-9743. **[ISI Proceedings] [3.2a]**

Vi21. Oana Boncalo, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu, Alexandru Amaricai. Error-Model Driven Analysis of Quantum Circuit's Reliability. Proc. 8th IEEE Conference on Nanotechnology, August 18-21, 2008, Arlington, USA, 18-21 August 2008, pp. 103-106, ISBN 978-1-4244-2104-6. **[INSPEC] [3.3b]**

Vi22. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Quantum Circuit Synthesis with Adaptive Parameters Control. Proc. European Conference on Genetic Programming (EuroGP), Tubingen, Germany, April 15-17, 2009, pp. 339-350, LNCS 5481, ISBN 978-3-642-01180-1. **[ISI Proceedings] [3.2a]**

Vi23. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Genetic Algorithm Based Quantum Circuit Synthesis with Adaptive Parameters Control. Proc. IEEE Congress on Evolutionary Computation (CEC), Trondheim, Norway, May 18-21 2009, pp. 896-903, ISBN 1-4244-2959-2. **[ISI Proceedings] [3.2a]**

Vi24. Flavius Opritoiu, Mircea Vladutiu, Mihai Udrescu, **Lucian Prodan**. Round-Level Concurrent Error Detection Applied to Advanced Encryption Standard. Proc. 12th IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems (DDECS), Liberec, Czech Republic, April 15-17 2009, pp. 270-275, ISBN 978-1-4244-3339-1. **[ISI Proceedings] [3.2a]**

Vi25. Flavius Opritoiu, Mircea Vladutiu, **Lucian Prodan**, Mihai Udrescu. Built-In Self Test Applicability for the Non-Linear Operations of Advanced Encryption Standard. Proc. 5th Intl. Symposium on Applied Computational Intelligence and Informatics (SACI2009), Timisoara, Romania, May 28-29 2009, pp. 307-312, ISBN 978-1-4244-4478-6. **[ISI Proceedings] [3.2b]**

Vi26. Flavius Opritoiu, Mircea Vladutiu, Mihai Udrescu, **Lucian Prodan**. Concurrent Error Detection for Multiplicative Inversion of Advanced Encryption Standard. Proc. 10th IEEE International Conference on Computer and Information Technology (CIT2010), Bradford, UK, June 29 – July 1 2010, pp.582-588, ISBN 978-0-7695-4108-2. **[INSPEC] [3.3b]**

Vi27. Flavius Opritoiu, Mircea Vladutiu, Mihai Udrescu, **Lucian Prodan**. A High-Speed AES Architecture Implementation. Proc. 7th ACM International Conference on Computing Frontiers (CF'10), Bertinoro, Italy, May 17 - 19 2010, pp. 95-96, ACM Press, ISBN 978-1-4503-0044-5. **[INSPEC] [3.3b]**

Vi28. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Performance Analysis for Genetic Quantum Circuit Synthesis. Proc. 10th International Conference on Artificial Intelligence and Soft Computing (ICAISC), Zakopane, Poland, June 13-17 2010, LNCS (Springer), vol. 6114, pp. 205-212, ISBN: 978-3-642-13231-5. **[ISI Proceedings] [3.2a]**

Vi29. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Adaptive vs. Self-adaptive Parameters for Evolving Quantum Circuits. Proc. 9th International Conference on Evolvable Systems: From Biology to Hardware (ICES 2010), LNCS (Springer), vol. 6274, York, UK, September 6-8 2010, pp. 348-359, ISBN: 978-3-642-15322-8. **[ISI Proceedings] [3.2a]**

Vi30. Septimiu Mare, Mircea Vladutiu, **Lucian Prodan**. HDR-based steganographic algorithm. Proc. 17th International Symposium for Design and Technology in Electronic Packaging (SIITME), Timisoara, Romania, Oct 20-23 2011, pp. 333-338, ISBN 978-1-4577-1277-7/11. **[INSPEC] [3.4b]**

Vi31. Septimiu Mare, Mircea Vladutiu, **Lucian Prodan**. Secret data communication system using Steganography, AES and RSA. Proc. 17th International Symposium for Design and Technology in Electronic Packaging (SIITME), Timisoara, Romania, Oct 20-23 2011, pp. 339-344, ISBN 978-1-4577-1277-7/11. **[INSPEC] [3.4b]**

Vi32. Septimiu Mare, Mircea Vladutiu, **Lucian Prodan**. Decreasing change impact using smart LSB Pixel mapping and data rearrangement. Proc. 11th IEEE International Conference on Computer and Information Technology (CIT2011), Paphos, Cyprus, Aug 31 – Sep 2 2011, pp. 269-276, ISBN 978-0-7695-4388-8/11. **[INSPEC] [3.3b]**

Vi33. Septimiu Mare, Mircea Vladutiu, **Lucian Prodan**. High Capacity Steganographic Algorithm Based on Payload Adaptation and Optimization. Proc. 7th International Symposium on Applied Computational Intelligence and Informatics (SACI), Timisoara, Romania, May 24-26 2012, pp. 87-92, ISBN 978-1-4673-1013-0. **[INSPEC] [3.4b]**

Vi34. Cristian Cosariu, **Lucian Prodan**, Mircea Vladutiu. Toward Traffic Movement Optimization Using Adaptive Inter-Traffic Signaling. Proc. 14th IEEE International Symposium on Computational Intelligence and Informatics (CINTI 2013), Budapest, Hungary, November 19-21, 2013, pp. 539-544, ISBN 978-1-4799-0194-4. **[ISI Proceedings] [3.2a]**

Vi35. Alexandru Iovanovici, **Lucian Prodan**, Mircea Vladutiu. Collaborative environment for road traffic monitoring. Proc. 13th International Conference on ITS Telecommunications (ITST 2013), Tampere, Finland, November 5-7, 2013, pp. 232-237, ISBN 978-1-4799-0846-2. **[ISI Proceedings] [3.2a]**

Vi36. Alexandru Iovanovici, Alexandru Topirceanu, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Heuristic Optimization of Wireless Sensor Networks Using Social Network Analysis. Proc. 6th International Workshop on Soft Computing Applications (SOFA 2014), Series: Advances in Intelligent Systems and Computing, Chapter: Soft Computing Applications, Vol. 356, 2015, pp 663-671, ISBN 978-3-319-18295-7. **[ISI Proceedings] [3.2a]**

Vi37. Alexandru Topirceanu, Alexandru Iovanovici, Cristian Cosariu, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Social Cities: Redistribution of Traffic Flow in Cities Using a Social Network Approach. Proc. 6th International Workshop on Soft Computing Applications (SOFA 2014), Series: Advances in Intelligent Systems and Computing, Chapter: Soft Computing Applications, Vol. 356, 2015, pp. 39-49, ISBN 978-3-319-18295-7. **[ISI Proceedings] [3.2a]**

Vi38. Alexandru Iovanovici, Cristian Cosariu, **Lucian Prodan**, Mircea Vladutiu. A hierarchical Approach in Deploying Traffic Lights Using Complex Network Analysis. Proc. 18th International Conference System Theory, Control and Computing (ICSTCC), Sinaia, Romania, October 17-19 2014, pp. 791-796. **[INSPEC] [3.4b]**

Vi39. Alexandru Iovanovici, Alexandru Topirceanu, Mihai Udrescu, **Lucian Prodan**, Stefan Mihaicuta. A high-availability architecture for continuous monitoring of sleep disorders. Proc. Medical Informatics Europe, Studies in health technology and informatics, vol. 210, pp. 729-733, 2015, ISSN 0926-9630. **[ISI Proceedings] [3.2a]**

Vi40. Cristian Cosariu, Alexandru Iovanovici, **Lucian Prodan**, Mihai Udrescu, Mircea Vladutiu. Bio-Inspired Redistribution of Urban Traffic Flow Using a Social Network Approach. IEEE Congress on Evolutionary

Computation (CEC), Sendai, Japan, 25-28 May 2015, pp. 77-84, ISBN 978-1-4799-7492-4. **[INSPEC] [3.4b]**

Vi41. Gabriel Baban, Alexandru Iovanovici, Cristian Cosariu, **Lucian Prodan**. Determination of the critical congestion point in urban traffic networks: a case study. 14th International Scientific Conference on Informatics, Poprad, Slovacia, 14-16 November 2017, pp. 18-23, doi: 10.1109/INFORMATICS.2017.8327215 **[ISI Proceedings] [3.2a]**

Vi42. Gabriel Baban, Alexandru Iovanovici, Cristian Cosariu, **Lucian Prodan**. High Betweenness Nodes and Crowded Intersections: An Experimental Assessment by Means of Simulation. IEEE 12th International Symposium on Applied Computational Intelligence and Informatics, Timisoara, Romania, 17-19 May 2018, pp. 339-344 doi: 10.1109/SACI.2018.8440989 **[ISI Proceedings] [3.2a]**

Vi43. Alexandru Iovanovici, Dacian Avramoni, Anca-Maria Moscovici, Lucian Prodan. Traffic micro-simulation as a validation tool for developing genetic algorithms for green wave optimization. IEEE 14th International Symposium on Applied Computational Intelligence and Informatics, Timisoara, Romania, 21-23 May 2020, pp. 147-152 doi: 10.1109/SACI49304.2020.9118841 **[ISI Proceedings] [3.2a]**

Vi44. Catalin Vladu, Lucian Prodan, Alexandru Iovanovici. Resource Constrained, Fast Convergence Training for Violence Detection in Video Streams. 22nd IEEE International Symposium on Computational Intelligence and Informatics (CINTI2022), Budapest, Hungary, 21-22 November, pp. 239-244 doi: 10.1109/CINTI-MACRo57952.2022.10029428 **[ISI Proceedings] [3.2a]**

Vi45. Dacian Avramoni, Roxana Virlan, Lucian Prodan, Alexandru Iovanovici. Detection of Pill Intake Associated Gestures using Smart Wearables and Machine Learning. 22nd IEEE International Symposium on Computational Intelligence and Informatics (CINTI2022), Budapest, Hungary, 21-22 November, pp. 251-256 doi: 10.1109/CINTI-MACRo57952.2022.10029615 **[ISI Proceedings] [3.2a]**

## Alte lucrări

E1. Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. A New Perspective in Simulating Quantum Circuits. Proc. ISGEC GECCO LBP, Chicago IL, USA, July12-16, 2003, ISBN 3-540-40603-4. **[3.5b]**

E2. Mihai Udrescu, Oana Boncalo, **Lucian Prodan**, Mircea Vladutiu, Alexandru Amaricai. Towards a Hybrid Methodology for Reliability Assessment of Quantum Circuits. Proc. Euromicro Work In Progress Session (held with SEAA and DSD), September 3-5, 2008, Parma, Italy, pp. 31-32, ISBN 978-3-902457-20-3. **[3.5b]**

E3. Cristian Ruican, Mihai Udrescu, **Lucian Prodan**, Mircea Vladutiu. Software Architecture for Quantum Circuit Synthesis. Proc. International Conference on Artificial Intelligence and Soft Computing (ICAISC), Zakopane, Poland, June 22-26 2008, published in Computational Intelligence: Methods and Applications, pp. 562-573, ISBN 978-83-60434-50-5. **[3.5b]**

E4. Flavius Opritoiu, Mircea Vladutiu, **Lucian Prodan**, Mihai Udrescu. AES Encryption-Decryption Optimized Datapath. Proc. 17th International Symposium for Design and Technology in Electronic Packaging (SIITME), Timisoara, Romania, Oct 20-23 2011, pp. 355-360, ISBN 978-1-4577-1277-7/11. **[3.6b]**

E5. Flavius Opritoiu, Mircea Vladutiu, Mihai Udrescu, **Lucian Prodan**. Fault Simulation Framework for Gate-Level and Transistor-Level Defects. Proc. 17th International Symposium for Design and Technology in Electronic Packaging (SIITME), Timisoara, Romania, Oct 20-23 2011, pp. 361-365, ISBN 978-1-4577-1277-7/11. **[3.6b]**

E6. Septimiu Mare, Mircea Vladutiu, **Lucian Prodan**, Flavius Opritoiu. Advanced steganographic algorithm using payload adaptation and graceful degradation. Proc. 1st International Conference on Information Engineering (ICIE 2012), Singapore, Lecture Notes in Information Technology (LNIT), vol. 25, June 26-28, 2012, pp. 121-127, ISBN: 978-1-61275-024-8/10, ISSN: 2070-1918. **[3.5b]**

E7. Dacian Avramoni, Alexandru Iovanovici, Cristian Cosariu, Iosif Szeidert-Subert, Lucian Prodan. Real time urban traffic data pinpointing most important crossroads. Proc. 8th International Workshop on Soft Computing Applications (SOFA 2018), Advances in Intelligent Systems and Computing, vol 1221, pp. 294-306, Springer, September 13-15 2018, Arad, Romania.

E8. Alexandru Iovanovici, Lucian Prodan, Dacian Avramoni. Traffic flow in 13 Romanian cities during and around lifting of COVID19 restrictions. Mendeley Data, V3, 14 Aug 2020, doi: 10.17632/g64s8h9k57.3 .